

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	24720	(printd adj (circuit or wiring) adj (board or substrate or layer)) or PCB or PWB	USPAT	OR	ON	2006/09/04 13:40
L4	12058	3 and (IC or chip or (semiconduct\$3 adj device))	USPAT	OR	ON	2006/09/04 13:41
L5	10	4 and (bonding with surface with roughness)	USPAT	OR	ON	2006/09/04 13:46
L6	1	("6359235").PN.	USPAT; USOCR	OR	OFF	2006/09/04 14:17
L7	8	(bond\$3 adj site) with roughness	USPAT	OR	ON	2006/09/04 14:19
L8	5	(through?hole or via?hole) with (conduct\$3 or wiring) with roughness	USPAT	OR	ON	2006/09/04 14:21
L9	56	((wiring or circuit) adj pattern) with roughness	USPAT	OR	ON	2006/09/04 14:22